

UNITED STATES DEPARTMENT OF COMMERCE United States Patent and Trademark Office Address: COMMISSIONER FOR PATENTS P.O. Box 1450 Alexandria, Virginia 22313-1450 www.uspto.gov

APPLICATION NO.	F	ILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/730,123		12/09/2003	Tatsuo Sengoku	009683-488	4345
21839	7590	01/11/2005		EXAM	INER
BURNS D		WECKER & M	IATHIS L L P	PRUCHNIC,	STANLEY J
		22313-1404		ART UNIT	PAPER NUMBER
	•			2859	

DATE MAILED: 01/11/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

TECHNOLOGY CENTER 2800

	Application No.	Applicant(s)
	10/730,123	SENGOKU ET AL.
Office Action Summary	Examin r	Art Unit
	Stanley J. Pruchnic, Jr.	2859
The MAILING DATE of this communication app Period for Reply	ears on the cover shet with the c	orrespondenc address
A SHORTENED STATUTORY PERIOD FOR REPLY THE MAILING DATE OF THIS COMMUNICATION. - Extensions of time may be available under the provisions of 37 CFR 1.13 after SIX (6) MONTHS from the mailing date of this communication. - If the period for reply specified above is less than thirty (30) days, a reply If NO period for reply is specified above, the maximum statutory period w - Failure to reply within the set or extended period for reply will, by statute, Any reply received by the Office later than three months after the mailing earned patent term adjustment. See 37 CFR 1.704(b).	36(a). In no event, however, may a reply be time within the statutory minimum of thirty (30) day will apply and will expire SIX (6) MONTHS from cause the application to become ABANDONE	nely filed s will be considered timely. the mailing date of this communication. D (35 U.S.C. § 133).
Status		
1) Responsive to communication(s) filed on 13 De	ecember 2004.	
· ·	action is non-final.	
3) Since this application is in condition for allowar closed in accordance with the practice under E	•	
Disposition of Claims		
 4) Claim(s) 1-15 is/are pending in the application. 4a) Of the above claim(s) 4-15 is/are withdrawn 5) Claim(s) is/are allowed. 6) Claim(s) 1-3 is/are rejected. 7) Claim(s) is/are objected to. 8) Claim(s) are subject to restriction and/or 	n from consideration.	
Application Papers		
9) The specification is objected to by the Examine	r.	
10)⊠ The drawing(s) filed on <u>09 December 2003</u> is/a	re: a)⊠ accepted or b)□ object	ted to by the Examiner.
Applicant may not request that any objection to the		`,'
Replacement drawing sheet(s) including the correct 11) The oath or declaration is objected to by the Ex		
Priority under 35 U.S.C. § 119		
12) Acknowledgment is made of a claim for foreign a) □ All b) □ Some * c) □ None of: 1. □ Certified copies of the priority documents 2. □ Certified copies of the priority documents 3. □ Copies of the certified copies of the priority application from the International Bureau * See the attached detailed Office action for a list	s have been received. s have been received in Applicat nity documents have been receive u (PCT Rule 17.2(a)).	ion No ed in this National Stage
Attachment(s)		
1) Notice of References Cited (PTO-892) 2) Notice of Draftsperson's Patent Drawing Review (PTO-948)	4) 🔲 Interview Summary Paper No(s)/Mail D	(PTO-413)
Notice of Draftsperson's Patent Drawing Review (P10-948) Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08) Paper No(s)/Mail Date 12/9/03(1SHEET).		ate Patent Application (PTO-152)

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DETAILED ACTION

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Priority

1. Receipt is acknowledged of papers submitted under 35 U.S.C. 119(a)-(d), which papers have been placed of record in the file.

Election/Restrictions

- 2. Applicant's election without traverse of Group I (Claims 1-3) in the reply filed on 13 December 2004 is acknowledged.
- 3. Claims 4-15 are withdrawn from further consideration pursuant to 37 CFR 1.142(b) as being drawn to a nonelected invention, there being no allowable generic or linking claim. Election was made without traverse in the reply filed on 13 December 2004.
- 4. Applicant is reminded that upon the cancellation of claims to a non-elected invention, the inventorship must be amended in compliance with 37 CFR 1.48(b) if one or more of the currently named inventors is no longer an inventor of at least one claim remaining in the application. Any amendment of inventorship must be accompanied by a request under 37 CFR 1.48(b) and by the fee required under 37 CFR 1.17(i).

Information Disclosure Statement

5. The references listed in the information disclosure statement (IDS) or PTO-1449 submitted by Applicant on 9 December 2003 are acknowledged. The cited references have been considered as indicated by the examiner's initials next to each reference considered.

However the foreign patent(s) and/or document(s) cited by applicant are considered only to the extent they could be understood from the abstract and drawings and Applicant's explanation of relevance in the Specification and partial translation.

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Claim Rejections - 35 USC § 103

6. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.

- 7. This application currently names joint inventors. In considering patentability of the claims under 35 U.S.C. 103(a), the examiner presumes that the subject matter of the various claims was commonly owned at the time any inventions covered therein were made absent any evidence to the contrary. Applicant is advised of the obligation under 37 CFR 1.56 to point out the inventor and invention dates of each claim that was not commonly owned at the time a later invention was made in order for the examiner to consider the applicability of 35 U.S.C. 103(c) and potential 35 U.S.C. 102(e), (f) or (g) prior art under 35 U.S.C. 103(a).
- 8. Claims 1-3 are rejected under 35 U.S.C. 103(a) as being unpatentable over Ebihara *et al.* (U. S. Patent No. 4,237,420, hereinafter **EBIHARA**) in view of JP7/326714 (hereinafter **NISHIGAKI**).

Regarding Claim 1, **EBIHARA** discloses a temperature detection circuit including:

a signal output circuit (22) outputting a first signal (a) having at least one rising or falling portion;

a delay circuit (18) formed of at least one inverter (18) to output a delayed version (f) of said first signal;

a logic circuit (32) receiving said first signal and said delayed version of said first signal;

a pulse width measurement circuit (23) outputting

a signal asserted (Rx) in response to

a signal received (b) from said logic circuit

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having a pulse with a width of no less than a predetermined width corresponding to a temperature desired to be detected; and a latch circuit (26) latching a signal output from said pulse width measurement circuit,

said pulse width measurement circuit having

an integration circuit (capacitor 16; resistor 14) receiving a signal output from said logic circuit and

a Schmitt trigger circuit (**34**) receiving a signal output from said integration circuit,

said Schmitt trigger circuit having a trigger potential set to have a value corresponding to said predetermined width.

EBIHARA as described above, does not explicitly disclose said circuit is a semiconductor integrated circuit as claimed by applicant in Claim 1. **EBIHARA** as described above, does not claim the components located on semiconductor integrated circuits as claimed by Applicant in claims 2-3.

NISHIGAKI discloses components on separate semiconductor integrated circuits, e.g., the buffer circuit (inverter) 50 is located on a CPU in order to detect that chip's internal temperature. Moreover, it is well known in the art of temperature sensing that the pulse width measurement circuit would be affected by temperature changes.

Therefore, it would have been obvious to one having ordinary skill in the art at the time the invention was made to shift the positions of these components, in order to keep the pulse width measurement circuit away from the hottest areas so it will have more accurate results, and based on the intended use for the temperature dependent oscillator to measure the temperature of the CPU chip as taught by **NISHIGAKI**.

Conclusion

- 9. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure. The prior art cited in a form PTO-892 and not mentioned above disclose related temperature sensing devices and methods of signal detection.
 - LEMMENS (U. S. Patent No. 5,708,375 A)
 - MEHNERT (U. S. Patent No. 4,873,518 A)
 - YIN (U. S. Patent No. 6,695,475 B2)
- 10. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Stanley J. Pruchnic, Jr., whose telephone number is (571) 272-2248. The examiner can normally be reached on weekdays (Monday through Friday) from 7:30 AM to 4:00 PM. If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Diego F. F. Gutierrez can be reached at (571) 272-2245.

The *Official FAX* number for Technology Center 2800 is **(703) 872-9306** for <u>all</u> <u>official</u> communications.

Any inquiry of a general nature or relating to the status of this application or proceeding may be directed to the official USPTO website at http://www.uspto.gov/ or you may call the USPTO Call Center at 800-786-9199 or 703-308-4357. The Technology Center 2800 Customer Service FAX phone number is (703) 872-9317.

The <u>cited U.S.</u> patents and patent application publications are available for download via the Office's PAIR. As an alternate source, <u>all U.S.</u> patents and patent application publications are available on the USPTO web site (<u>www.uspto.gov</u>), from the Office of Public Records and from commercial sources.

Private PAIR provides external customers Internet-based access to patent application status and history information as well as the ability to view the scanned images of each customer's own application file folder(s).

For inquiries relating to Patent e-business products and service applications, you may call the *Patent Electronic Business Center (EBC)* at 703-305-3028 or toll free at 866-217-9197 between the hours of 6 a.m. and midnight Monday through Friday EST, or by e-mail at: ebc@uspto.gov. Additional information is available on the Patent EBC Web site at: http://www.uspto.gov/ebc/index.html.

Stanley J. Pruchnic, Jr. 1/10/05

DIEGO F. F. GUTIERREZ SUPERVISORY PATENT EXAMINER TECHNOLOGY CENTER 2800

G. Olrhaffuz GAIL VERBITSKY PRIMARY EXAMINER

	1449A/PTO &	

FIRST INFORMATION DISCLOSURE STATEMENT BY APPLICANT

	
ATTORNEY'S DKT NO. 009683-488	APPLICATION NO. Unassigned
APPLICANT Tatsuo SENGOKU et al.	10/730,123
FILING DATE	GROUP 2859

Examiner	Document	Kind Code	Name o	of Palentee or Ar	policant	Issue/Public	ation
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*Also discuss d in the specification

Notice of References Cited Application/Control No. | Applicant(s)/Patent Under Reexamination | SENGOKU ET AL. | Examiner | Art Unit | Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	А	US-6,695,475 B2	02-2004	Yin, Rong	374/171
	В	US-5,708,375 A	01-1998	Lemmens, Hubertus G. C.	327/36
	С	US-4,873,518 A	10-1989	Mehnert, Walter	327/31
	D	US-4,237,420 A	12-1980	Ebihara et al.	374/170
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FOREIGN PATENT DOCUMENTS

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NON-PATENT DOCUMENTS

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A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).) Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.

